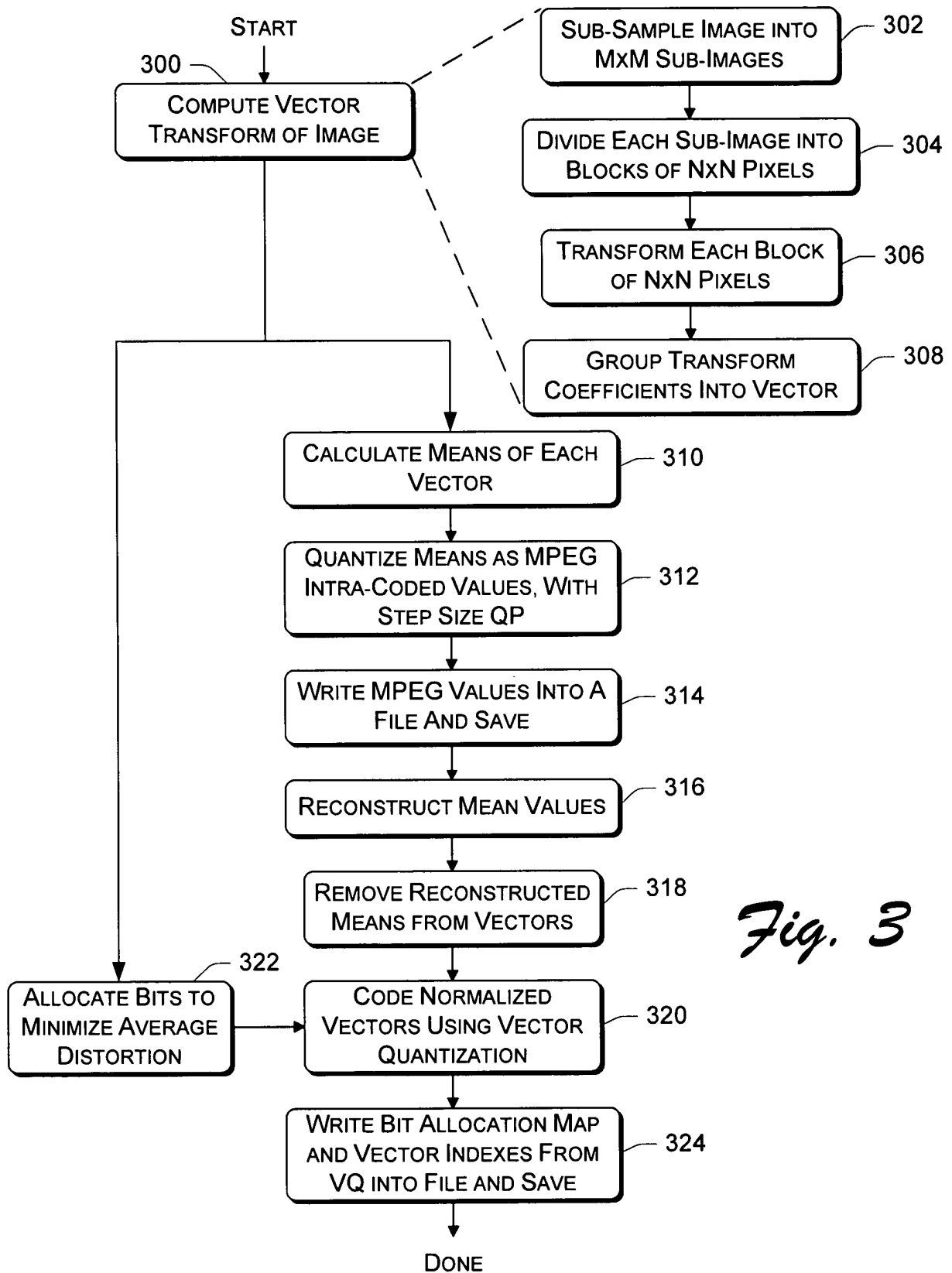
*Fig. 2*

STEP 200 (FIG 2): VTQ CODING*Fig. 3*

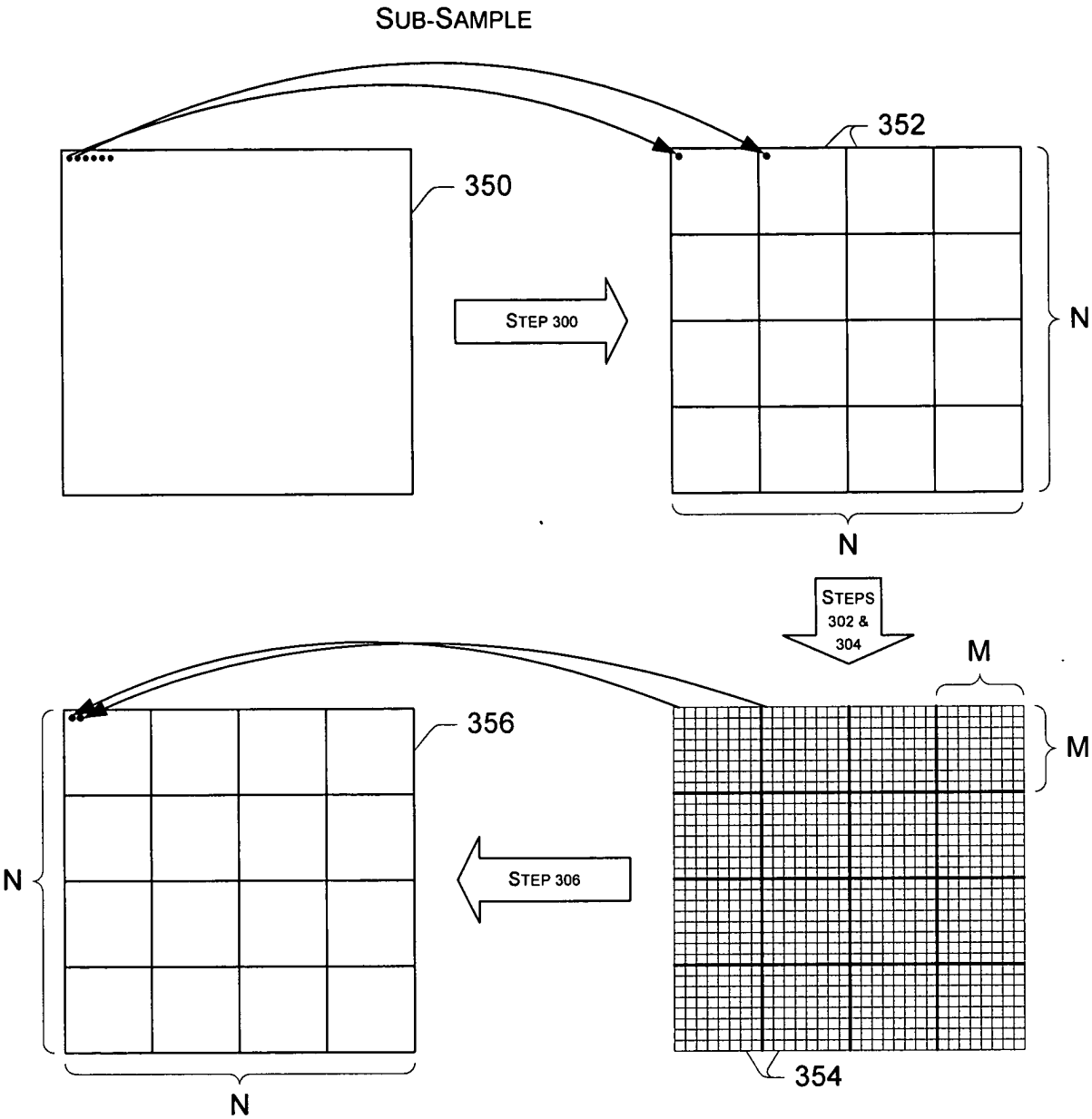
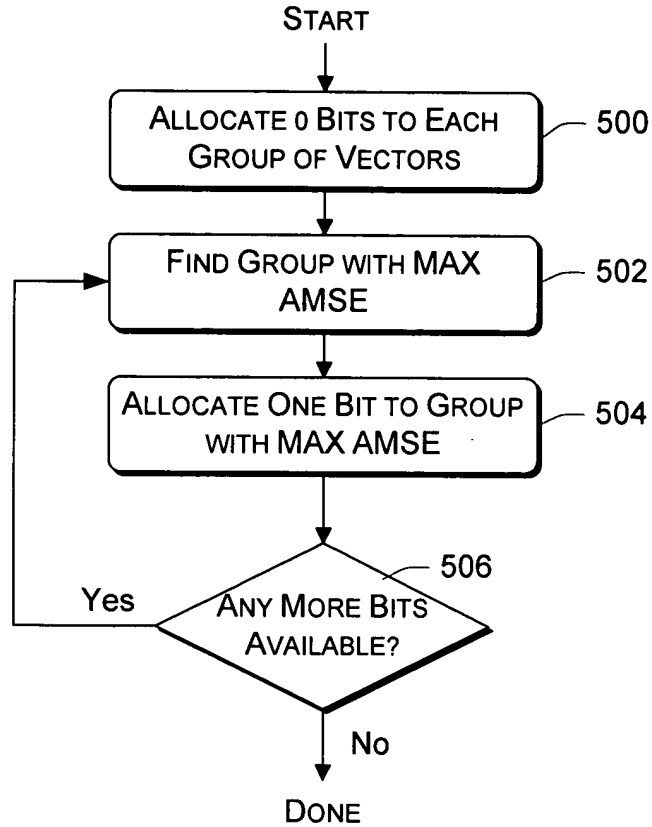


Fig. 4

STEP 322 (FIG 3): BIT ALLOCATION*Fig. 5*

520								522								524								
6	6	6	6	6	5	5	5	9	9	9	9	8	8	8	8	1	1	1	1	1	1	1	1	1
5	5	5	5	5	4	4	4	9	8	8	8	8	8	8	8	2	2	2	2	2	2	2	2	2
5	4	4	4	4	4	4	4	9	8	8	8	8	8	8	8	1	1	1	1	1	1	1	1	1
5	4	4	4	4	4	4	4	8	8	8	8	8	8	8	8	2	2	2	2	2	2	2	2	2
4	4	4	4	4	4	3	4	8	8	8	8	8	8	8	8	1	1	1	1	1	1	1	1	1
4	4	3	4	4	3	4	3	8	8	8	8	8	8	8	7	2	2	2	2	2	2	2	2	2
4	3	3	4	3	3	3	3	8	8	8	8	8	8	7	7	1	1	1	1	1	1	1	1	1
4	3	3	3	3	3	3	3	8	8	8	8	8	7	7	7	2	2	2	2	2	2	2	2	2
4 BITS/VECTOR								8 BITS/VECTOR								12 BITS/VECTOR								

Fig. 6

**STEP 202 (FIG 2): CODEBOOK
GENERATION AND ORDERING**

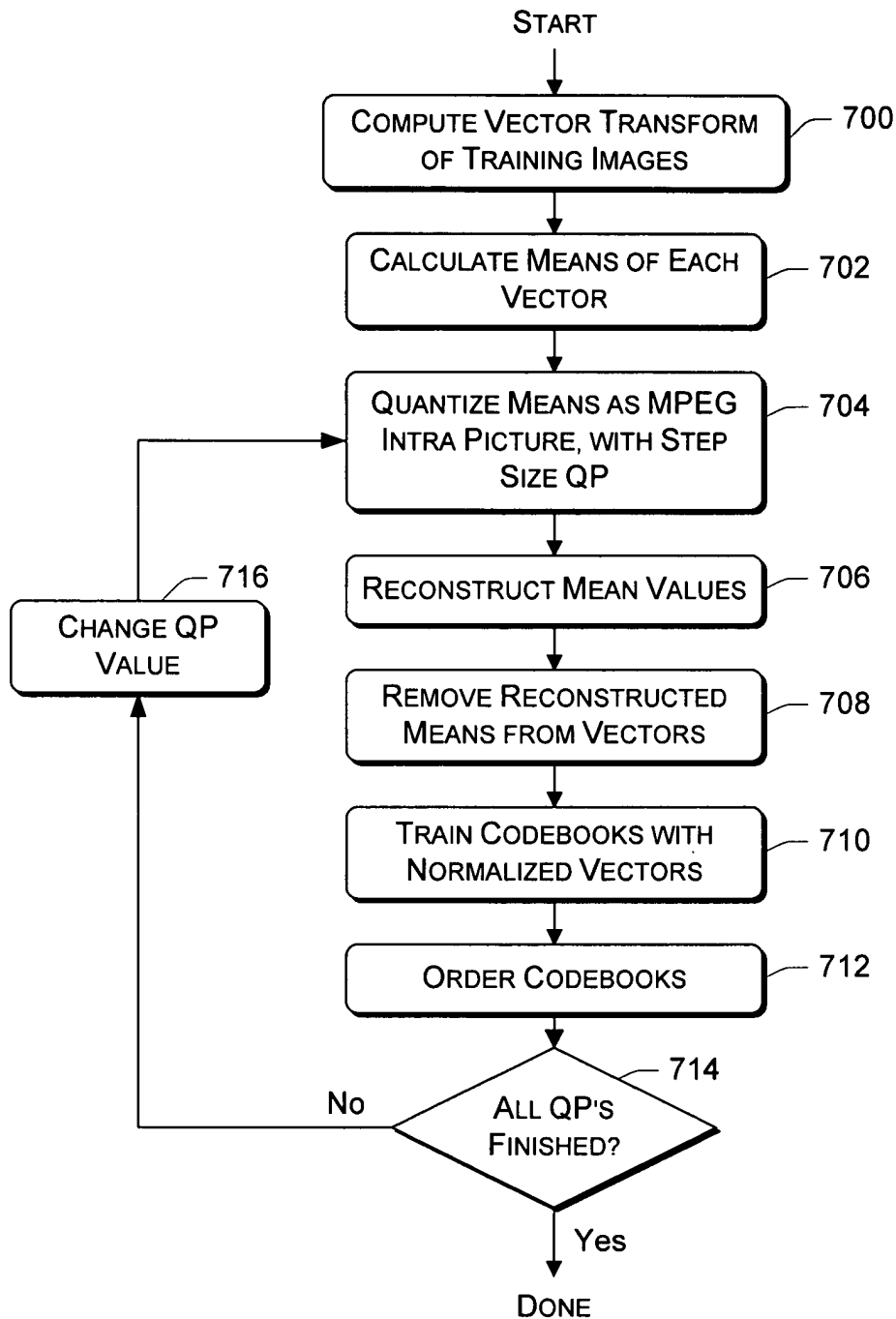


Fig. 7

ORIGINAL CODEBOOK

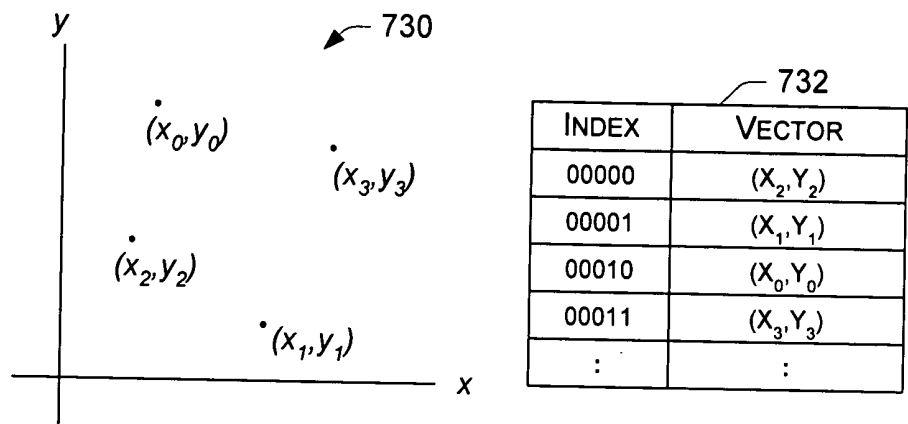


Fig. 8

ORDERED CODEBOOK

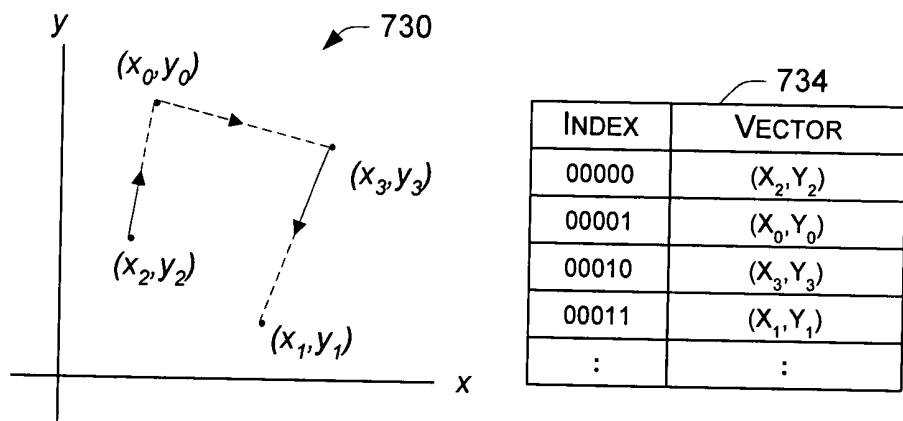


Fig. 9

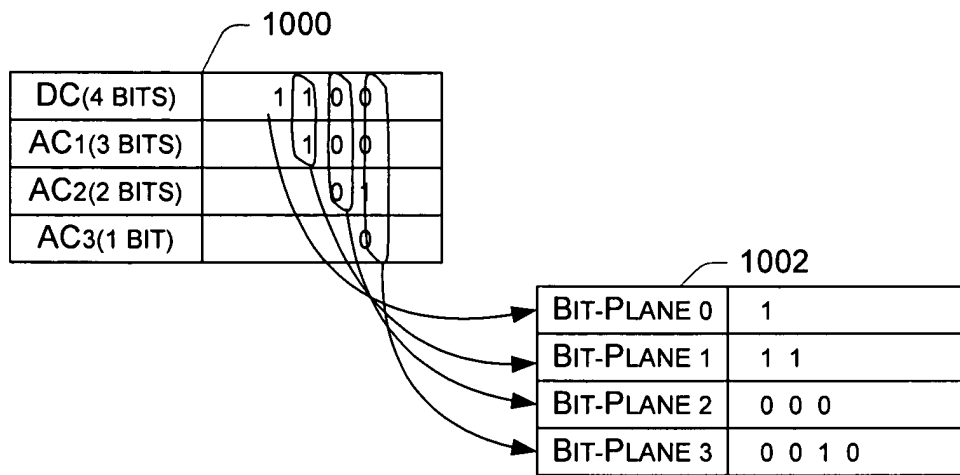


Fig. 10

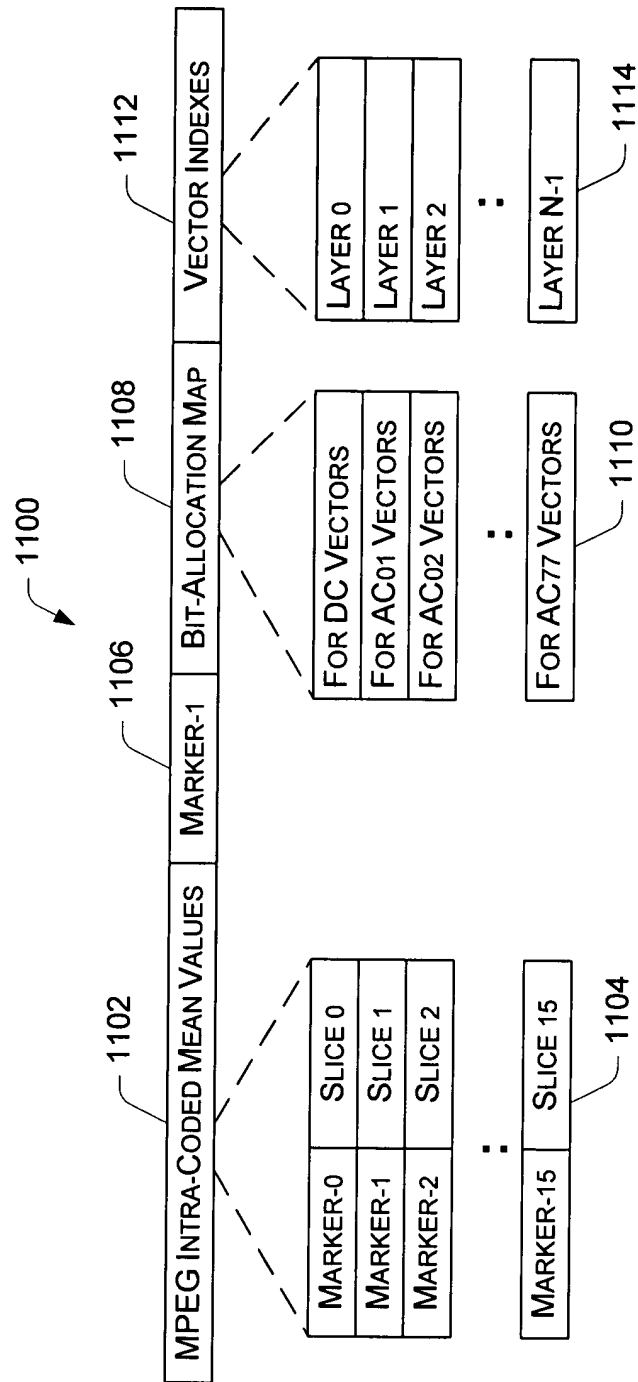
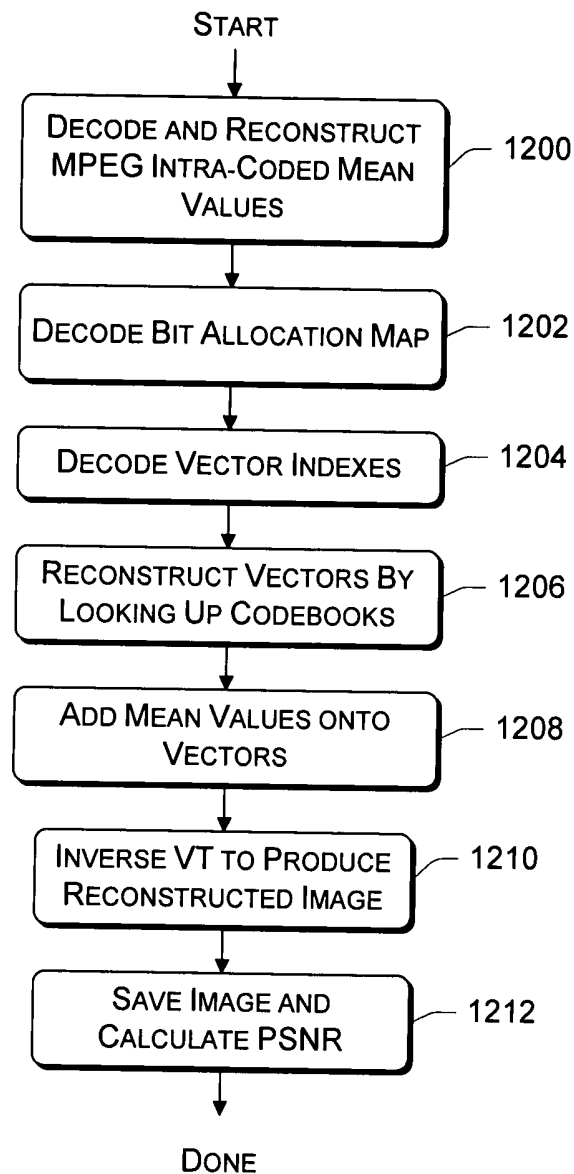


Fig. 11

STEP 210 (FIG 2): VTQ DECODING*Fig. 12*